

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. **UTILITY** Patent Application

SCANNED <i>KS</i> O.I.P.E. <i>11/13</i> Q.A. <i>Edu</i>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/938686		257- 438	106	2811	H. VU

APPLICANTS

Angel Pape
James Yamaguchi

TITLE

Stackable layers containing encapsulated integrated circuit chips with one or more overlying interconnect layers and a method of making the same

PTO-2040
12/99

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Dwg.	Figs. Dwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	
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